

**ELECTROMAGNETICALLY COUPLED INTERCONNECT
SYSTEM ARCHITECTURE**

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Electromagnetically Coupled Interconnect System Architecture

Background of the Invention

Field of the Invention

[0001] The present invention relates in general to an electromagnetic contactless interconnect scheme for providing a communication path between electronic components, such as integrated circuits, and/or electrical systems and, in particular, to an electrical interconnect scheme in which the electronic elements are electromagnetically coupled to each other either directly or through an intermediate device, by contactless proximity connections.

Description of Related Art

[0002] Integrated circuits and other elements of an electronics system typically communicate with one another through a wired interconnection structure. For example, in a data processing or computing system, a parallel wired interface, such as a bus, may link a microprocessor to other integrated circuits, such as memory integrated circuits, within the system. To communicate with one another, all of the integrated circuits and other electronic elements of the system must be physically connected, with a direct current (DC) path, to the wired interconnection structure. In other words, the integrated circuits and other electronics elements must make physical contact with the wired interconnection structure. Thereafter, the integrated circuits and other system elements can send electronic signals to each other over the wired interconnection structure.

[0003] In a wired interconnection structure, each element sends signals on the wired interconnection structure at any given time, but all integrated circuits and system elements typically monitor each signal traveling on the wired interconnection structure. This is a limitation of wired

system element ignores data conveyed on the wired interconnection structure unless the data is addressed to that integrated circuit or system element.

[0004] In a typical wired interconnection structure, each wired signal line is usually implemented by a separate trace on a printed circuit board or the like. Drivers and receivers within each integrated circuit or system element transmit and receive signals conveyed on each line of the wired interconnection structure. The drivers and receivers do so by physically contacting the lines and thereby creating an electrical connection with the lines. Such prior methods, however, have several disadvantages: they are costly, they consume power, they can distort and attenuate high frequency signals, and they often require large, capacitive electrostatic discharge (ESD) protection devices. In many high frequency applications, the signal distortion caused by the wired interconnection structure, rather than the speed of the integrated circuits or system elements themselves, often limits the speed or data rate at which the integrated circuits and system elements are able communicate with each other.

Brief Summary of the Invention

[0005] The present invention is directed to a method and apparatus for effecting contactless, proximity connections between elements in an electronics system.

[0006] In one embodiment, a plurality of electronic components, such as integrated circuits, are electromagnetically coupled to a transmission line. A first

... communicated from the first electronic component to the transmission line and then from the transmission line to the other electronic component by electromagnetic coupling, eliminating the need for physical contact between either

electronic component and the transmission line. In other embodiments, electronic components, such as integrated circuits, are electromagnetically coupled to each other directly, obviating the need for an intermediary, such as a transmission line.

Brief Description of the Drawing(s)

[0007] Figure 1 illustrates an exemplary embodiment of the invention in which integrated circuits are electromagnetically coupled to a transmission line.

Figure 2 is a plan view illustrating the integrated circuits of Figure 1 on a printed circuit board.

Figure 3 is a partial, cross-sectional view of the integrated circuits and printed circuit board of Figure 2.

Figure 4 illustrates a block diagram of an exemplary integrated circuit that may correspond to the integrated circuit 14 of Figure 1.

Figure 5 illustrates an exemplary embodiment of the invention in which eight integrated circuits are electromagnetically coupled to a transmission line.

Figures 6a and 6b illustrate exemplary transmitter and receiver circuits that may correspond to the transceiver 16 in Figure 4.

Figure 7 illustrates exemplary coupling characteristic data for an exemplary embodiment of the invention.

Figure 8 illustrates a block diagram of an exemplary integrated circuit that may correspond to an integrated circuit that may be used with a plurality of contactless interconnects of the invention.

Figure 9 illustrates an exemplary embodiment of the lines.

Figure 10 is a cross-sectional view from Figure 9.

Figure 11 is a cross-sectional, side view of an embodiment of the invention in which daughter cards are electromagnetically coupled to a mother board.

Figure 12 is a detailed cross-sectional view of the connector elements of Figure 11.

Figure 13 illustrates an exemplary integrated circuit that may be used in the embodiment of the invention illustrated in Figure 14.

Figure 14 illustrates a cross-sectional view of an exemplary embodiment of the invention in which a plurality of integrated circuits are electromagnetically coupled to a ring bus structure.

Figure 15 illustrates a cross-sectional view of an exemplary embodiment of the invention in which a plurality of stacked integrated circuits are electromagnetically coupled.

Figure 16 illustrates an exemplary embodiment of the invention in which dual sides of an integrated circuit are electromagnetically coupled.

Figures 17a-17c illustrate exemplary embodiments of the invention in which two or more integrated circuits are directly electromagnetically coupled.

Figure 18 illustrates an exemplary embodiment of an integrated circuit having a spiral electromagnetic coupler.

Figure 19 illustrates an equivalent circuit diagram corresponding to Figure 18.

Detailed Description of the Invention

[0008] The present invention is directed to a method and apparatus for effecting contactless, proximity connections between elements in an electronics system. (As used herein,

"contactless" means that direct electrical contact between conductors is not required.) The following describes exemplary embodiments of the invention. The invention, however, is not limited to the following exemplary embodiments and their variations.

in which the exemplary embodiments operate or are described herein.

[0009] Figures 1 through 4 illustrate an exemplary embodiment of the invention. As shown in Figure 1, an electronics system 10 includes a plurality of integrated circuits 14(1)-14(x) and a transmission line 22. As shown in Figure 1, a transmission line typically includes return line 23. As illustrated in Figures 2 and 3, the integrated circuits 14(1)-14(x) are mounted on a printed circuit board 21, and the transmission line 22 is embedded in the printed circuit board. Alternatively, transmission line 22 may be located on a surface of the printed circuit board 21. As illustrated in Figure 1, the transmission line 22 is preferably terminated in its characteristic impedance 27, 29 to reduce or eliminate reflections.

[0010] Integrated circuits 14(1)-14(x) may be any type of integrated circuit or electronic circuit. For example, one or more of integrated circuits 14(1)-14(x) may be without limitation a memory device, a microprocessor, a microcontroller, a digital logic device, an analog device, or any combination of the foregoing. Figure 4 illustrates a block diagram of an exemplary integrated circuit 14 that may be used with the present invention. Although not part of integrated circuit 14, transmission line 22 is also shown in Figure 4 for clarity and discussion purposes.

[0011] As shown in Figure 4, integrated circuit 14 may include a logic circuit 12 that implements the function of the input/output interface 11. The input/output interface 11 provides signals to and from the logic circuit 12. Such signals may be any type of analog or digital signals. For example, in a data processing or computing system, the signals may include, without limitation, data signals, control signals, and address signals.

signals, timing signals, clock signals, and the like. As used herein, the terms "data" and "signals" are intended to include all such signals.

[0012] Each integrated circuit 14 may also include a radio frequency (RF) transceiver 16 and a small electromagnetic coupler 18. Preferably, the electromagnetic coupler is sufficiently small that it can be formed on or within the integrated circuit using standard semiconductor fabrication techniques. Alternatively, the electromagnetic coupler 18 could be fabricated as part of the semiconductor package. Thus, the electromagnetic coupler is preferably smaller than a typical semiconductor die. In the embodiment illustrated in Figures 1-4, input/output interface 15 preferably provides a serial interface to the transceiver 16, and transceiver 16 encodes the data received from input/output interface 15 using any suitable RF modulation scheme. Nonexclusive examples of suitable RF modulation schemes include amplitude modulation (AM), frequency modulation (FM), phase code modulation (PCM), phase modulation (PM), or any combination of the foregoing. It is believed that modulation schemes used in modem technology may be particularly advantageous in the present invention. However, the specific design of the transceiver and the specific modulation scheme are not critical to the invention, and any suitable transceiver and modulation scheme may be used with the present invention.

[0013] Transceiver 16 provides the modulated signal to electromagnetic coupler 18. Electromagnetic coupler 18 is preferably formed on or as part of the integrated circuit 14.

impedance 19 to obtain desired directional coupling, power, and distortion characteristics. Furthermore, the electromagnetic coupler 18 may be terminated or connected to a reference voltage other than ground for improved operation.

[0014] Electromagnetic coupler 18 is disposed in proximity to transmission line 22 so as to be electromagnetically coupled to transmission line 22. Preferably, electromagnetic couplers 18 are disposed within approximately ten millimeters of the transmission line 22. The invention is not limited, however, to placement of any electromagnetic coupler 18 within ten millimeters of the transmission line 22. Transmission line 22 is typically embedded in or located on printed circuit board 21. Because electromagnetic coupler 18 is electromagnetically coupled to transmission line 22, the modulated signal provided to electromagnetic coupler 18 by transceiver 16 induces a similar but attenuated signal in the transmission line. A contactless communication path or channel is thus provided between integrated circuit 14 and transmission line 22.

[0015] The transmission line 22 may be any type of transmission line including without limitation a microstrip line, a strip line, a twisted pair, a coaxial cable, a wire over ground, a waveguide, or any combination, hybrid, or modification of the foregoing. The specific design or implementation of the transmission line 22 is not critical to the invention, and indeed, any structure capable of being electromagnetically coupled with an electromagnetic coupler 18 and conducting or channeling a received signal may function as a transmission line 22 in the present invention.

[0016] Regardless of its specific implementation, the transmission line 22 is preferably embedded within a printed circuit board 21. However, the transmission line may be formed

above, in order to prevent or minimize reflections, the transmission line 22 is preferably terminated at one or both ends in its characteristic impedance 27, 29.

[0017] The printed circuit board 21 is preferably a typical printed circuit board as commonly used in the electronics field. The design and composition of the printed circuit board 21, however, is not critical to the invention and may be any substrate capable of supporting electronic components and on which or within which transmission lines or conductors can be attached or formed.

[0018] Modulated signals induced on transmission line 22 by one integrated circuit 14(1) may be detected by another integrated circuit 14(x) in the system 10. That is, the modulated signal in transmission line 22 induces a similar but attenuated signal in the electromagnetic coupler 18(x) of the other integrated circuit or circuits 14(x) whose electromagnetic couplers 18(x) are disposed in proximity to the transmission line 22 so as to be electromagnetically coupled to the transmission line.

[0019] Assuming that the integrated circuit is configured as illustrated in Figure 4, a modulated signal sensed by an electromagnetic coupler 18 is decoded (demodulated) by transceiver 16. The decoded data is then provided to input/output bus 15, which provides the data to logic circuit 12.

[0020] It should be noted that coupling between an electromagnetic coupler 18 and transmission line 22 can optionally be made directional by terminating the grounded end of the electromagnetic coupler 18 in the characteristic impedance 19 of the electromagnetic coupler, as illustrated in

impedance 19 as shown in Figure 4) and which end is connected to transceiver 16, electromagnetic coupler 18 can induce an RF signal traveling only in one direction along transmission line

22 and can receive an RF signal traveling only in the opposite direction along transmission line 22.

[0021] For example, an electromagnetic wave traveling on transmission line 22 such that its wave front first passes the grounded end of coupler 18 and thereafter passes the end of coupler 18 connected to transceiver 16 will generate a signal in coupler 18 that is detected by transceiver 16. On the other hand, an electromagnetic wave traveling in the opposite direction along transmission line 22 will generate a wave in coupler 18 that is dissipated by impedance 19; transceiver 16 will not detect such a wave.

[0022] If impedance 19 is not present (e.g., coupler 18 is grounded or open-circuited), the wave generated in coupler 18 would reflect off of the end of coupler 18 back into transceiver 16. Thus, without impedance 19, waves traveling in either direction on transmission line 22 are detected by transceiver 16.

[0023] Regardless of whether impedance 19 is present, electromagnetic waves generated by the transmitter portion of transceiver 16 will propagate along coupler 18 from the transceiver to the grounded end of the coupler. The wave propagating along coupler 18 will cause a wave to be generated in transmission line 22 in the same direction. If impedance 19 is not present, the wave in coupler 19 will reflect off of the grounded end of coupler 18 and propagate back toward transceiver 16. The reflected wave will generate a wave in transmission line 22 in the same direction as the reflected

[0024] If, however, impedance 19 is present, the initial wave generated by coupler 18 will not reflect along coupler 18 back toward transceiver 16. Hence, the initial wave will be

dissipated by impedance 19. In such a case, a wave is generated in transmission line 22 in only one direction. Thus, if impedance 19 is present, the transmitter portion of transceiver 16 will create waves in transmission line 22 in only one direction.

[0025] Directional coupling between coupler 18 and transmission line 22, as discussed above, may be advantageous when, for example, the logic circuit 12 of integrated circuit 14(1) is a microprocessor, and the logic circuits 12 of the other integrated circuit or circuits 14(x) are memories or other devices that communicate with the microprocessor but not with one another. An example of such a case is discussed below with respect to Figure 5. In such a case, the electromagnetic coupler 18 of integrated circuit 14(1) may be oriented to transmit signals to the right on transmission line 22 and to receive signals traveling to the left of transmission line 22. The electromagnetic couplers 18 of integrated circuit or circuits 14(x) would be oriented to transmit signal to the left and to receive signals transmitted to the right. Such directional coupling can limit the load drawn by integrated circuit or circuits 14(x) when any one of those integrated circuits is transmitting to integrated circuit 14(1). Of course, coupling between an electromagnetic coupler 18 and transmission line 22 can be made bi-directional by simply leaving electromagnetic coupler 18 open circuited or grounded.

[0026] A simple, exemplary transceiver circuit is illustrated in Figures 6a and 6b. Figure 6a illustrates an exemplary transmitter 300 portion of the circuit, and Figure 6b

illustrates an exemplary receiver 302 portion of the circuit. A square wave carrier signal is input at terminal 304 of XOR gate 306. The square wave carrier signal may be a system clock signal. The output 308 of XOR gate 306 is a bipolar phase shift keying (BPSK) modulated signal containing a digital

data and the clock to be transmitted. Resistor 310 controls the amount of current that will flow through coupling loop 312. Coupling loop 312 radiates electromagnetic energy corresponding to the modulated signal, which, as discussed above, induces a similar but attenuated modulated signal in any other coupling loop or transmission line that is electromagnetically coupled to coupling loop 312.

[0027] In the exemplary receiver circuit 400 illustrated in Figure 6b, an attenuated modulated signal is generated in coupling loop 402 by transmission of the modulated signal by any other coupling loop or transmission line electromagnetically coupled to coupling loop 402. The modulated signal is amplified by amplifier 404. The amplified, modulated signal 406 is demodulated by bit synchronizer 408. If bit synchronizer 408 requires a phase-locked-loop circuit, it may be feasible to use the phase-locked-loop circuit that is typically found in most integrated circuits. Bit synchronizer 408 outputs demodulated data and clock signals from the modulated signal at output 410. In addition, if a system clock signal was used by a transmitter to modulate the transmitted signal, the bit synchronizer clock output may also be used as a system clock signal at output 412. Other bit synchronizer clock recovery schemes may be used including without limitation delay lock loops and early-late discriminators.

[0028] It should be stressed that the above described transceiver design is exemplary only. The specific design of the transceiver is not critical to the invention, and any suitable transceiver may be used with the invention.

[0029] In one embodiment of the invention, two or more integrated circuits in system 10 may communicate with each other without requiring a physical electrical contact of conductors. All or part of the path between an electrical transmitter and a receiver may be

integrated circuit 14(1) and the electromagnetic coupler 18 an another integrated circuit 14(x) may be referred to as a contactless communication channel or path.

[0030] Figure 5 illustrates an exemplary electronics system 11 in which eight integrated circuits 14(1) through 14(8) are electromagnetically coupled to transmission line 22. For example, the eight integrated circuits may be a microprocessor 14(1) and seven memory devices 14(2)-14(7). The eight integrated circuits 14(1) through 14(8) are mounted on a printed circuit board (not shown in Figure 5). Each of the electromagnetic couplers 18(1) through 18(8) are electromagnetically coupled to transmission line 22. The system 11 may be partially or fully shielded. Exemplary shielding configurations are described more fully below.

[0031] An exemplary manner in which system 11 may operate is as follows. In this example, integrated circuit 14(1), a microprocessor, wishes to write data to integrated circuit 14(4), in this example, a memory device. To do so, integrated circuit 14(1) modulates the following: the data to be written to the memory, a write command code, and an address identifying both the memory device 14(4) and the location within memory device 14(4) to which the data is to be written into a carrier signal. Because electromagnetic coupler 18(1) is electromagnetically coupled to transmission line 22, the modulated signal in electromagnetic coupler 18(1) generates a similar, though attenuated signal, on the transmission line, which in turn generates a similar, though still further attenuated, signal in each of the electromagnetic couplers

this example, receives the data, write command, and the address transmitted by microprocessor 14(1). Because the address identifies memory device 14(4) as the intended recipient of the transmitted signal, memory device 14(4) keeps until it receives the

data. Of course, if the system implementation requires higher data rates than a single channel can support, a multiplicity of transmission lines and channels can be utilized.

[0032] Although not required in the present invention, shielding materials may be disposed in electronics system 10 so as to partially or completely shield the contactless communication channels or paths shown in Figures 1-3 and described above.

[0033] For example, as illustrated in Figures 2 and 3, a shielding plane 38 may shield the circuitry on the integrated circuit 14 from the electromagnetic coupler 18. (Figure 2 is a plan view of the plurality of integrated circuits 14(1)-14(x) of Figure 1 mounted on an upper surface of printed circuit board 21, and Figure 3 is a partial sectional elevation view of the printed circuit board 21 and integrated circuits 14(1) and 14(x) of Figure 2.) As shown in Figure 3, the active circuit elements of the integrated circuit (e.g., the logic circuit 12, the input/output interface 15, and the transceiver 16 illustrated in Figure 4) are fabricated on a die 32 that includes a semiconductor substrate and various metalization and insulating layers formed thereon.

[0034] A shielding plane 38 is disposed between the circuitry on the die and the electromagnetic coupler 18. The shielding plane may be any type of conductive material suitable for absorbing or blocking electromagnetic signals. Insulating layers 34 and 36 may be formed on the die 32 surrounding the shielding plane 38. In the exemplary embodiment of Figure 3, a

shielding plane 38 is disposed between the transceiver 16 of Figure 4) with electromagnetic coupler 18. Although not required by the present invention, the shielding plane 38 may be grounded and may supply connections to ground to the electromagnetic coupler 18 and the integrated circuit 14.

Alternatively, the shielding plane 38 may be electrically connected to a voltage supply and provide power or a reference voltage to integrated circuit 14.

[0035] One or more shielding planes may also be provided in or on the printed circuit board 21. For example, a shielding plane 46 may be embedded in or formed on printed circuit board 21 between the transmission line 22 and the integrated circuit 14. As illustrated in the exemplary embodiment of Figure 3, one or more gaps or "windows" 50 in the shielding plane 46 allow electromagnetic coupling through gap(s) 50 between the electromagnetic coupler 18 and the transmission line 22. Again, the shielding plane 46 may be connected to ground, a reference voltage, or power and used to provide ground connections, a reference voltage, or power to the printed circuit board 21 or the integrated circuit 14. Shielding plane 38 may also act as a lid or cover for the gaps 50 that prevent radiation from either the coupling loop 18 or the transmission line 22 from affecting circuitry on the die.

[0036] Another shielding plane 48 may be provided within or on the printed circuit board 21 such that the transmission line 22 is disposed between the shielding plane 46 and the shielding plane 48. Again, these shielding planes 46, 48 may be connected to ground, a reference voltage, or power and used to provide ground connections, the reference voltage, or power to the printed circuit board 21 or the integrated circuit 14. As shown in Figure 3, insulating layers, such as layers 40, 41, 42, and 43, may also be included in or on the printed circuit board 21.

[0037] Thus, the shield planes 38, 46, 48 may be used to shield communication paths from an electromagnetic coupler 18 on one integrated circuit 14 to the electromagnetic coupler 18 on another integrated circuit. Additional shielding planes between the shield planes 38, 46, 48 may be provided to shield communication paths

to more completely shield the communication path. For example, additional shielding could be provided around transmission line 22 to more complete shield the transmission line. In addition, shielding material may be disposed around electronics system 10 itself to fully or partially "close" the entire system.

[0039] Many multiplexing, data exchange, and communication schemes and protocols are known in the electronics fields, and any such scheme or schemes or combination thereof may be used with the above described embodiment for transmitting data between integrated circuits. For example, known multiplexing schemes include without limitation time division multiplexing, frequency division multiplexing, and code division multiplexing. Exemplary, known protocols include without limitation Scalable Coherent Interface (SCI), Fire Wire, Ethernet, and Universal Serial Bus. Again, any such multiplexing scheme or protocol or combination thereof may be used with the instant invention.

the transmission line 22; the physical orientation of the electromagnetic coupler 18 to the transmission line 22; the length of the electromagnetic coupler 18 relative to the wavelength of the carrier signal; the shapes of the electromagnetic coupler 18 and the transmission line 22. Using these and other parameters affecting coupling known to persons skilled in the field, the attenuation of signals wirelessly passed between the electromagnetic couplers 18 and the transmission line 22 can be preselected and designed into the system 10.

[0041] It should be noted, however, that when electromagnetic couplers 18 of a large number of integrated circuits 14 are tightly coupled (that is, coupled so as to reduce substantially the amount of attenuation) to a transmission line 22, each electromagnetic coupler draws a substantial amount of power from the RF signal as it travels along transmission line 22 and the RF signal can become severely attenuated by the time it reaches an integrated circuit at the end of transmission line 22. In such a case, it is preferable to design electromagnetic couplers 18 to be less tightly coupled to transmission line 22 so that they do not draw substantially more power than needed to permit an incoming RF signal to be properly detected by transceivers 16. Thus, generally speaking, loose coupling is preferred over tight coupling, particularly in systems where many devices share a common channel. However, in systems where only a small number of devices are coupled together, tighter coupling may be desired to reduce attenuation between devices and reduce undesirable radiation. For example, tighter coupling may be

[0042] Table I below summarizes three link budget analysis applied to a wide range of operating conditions for the system illustrated in Figure 1 and 2. When the system

coupling, particularly in systems where many devices share a common channel. However, in systems where only a small number of devices are coupled together, tighter coupling may be desired to reduce attenuation between devices and reduce undesirable radiation. For example, tighter coupling may be appropriate in systems having eight or fewer electronic devices electromagnetically coupled to a transmission line.

[0042] Table I below summarizes three link budget analysis applicable over a broad range of operating conditions for the embodiment illustrated in Figure 3 above, given the following exemplary parameters. A carrier frequency in the range of 1-10 GHz is assumed, and electromagnetic couplers 18 are about 2-3 millimeters long and about 50 microns above shielding plane 46. Insulating layers 36 and 38 together are about 25 microns thick. Transmission line 22 is about 150 micron wide, spaced about 150 microns from shielding planes 46 and 48. It should be stressed that the above dimensions are exemplary only and given as the framework setting for the below described exemplary link budget analyses. The invention is not limited in any way to the above described dimensions or the below described operating ranges.

[0043] Exemplary case #1 through case #3 of table I represent decreasing system cost and complexity at the expense of decreasing data rate performance.

[0044] The Noise power N_i in milliwatts is given by the formula:

Where:

$$k = 1.38 \times 10^{-23} \text{ Joules/Degree}$$

(Boltzmann's constant)

$$T_e = F \cdot T_0$$

scheme in a modest implementation. More complex modulation schemes and circuitry are capable of yielding higher bits / Hz densities. Likewise, spread spectrum techniques can yield lower bit / Hz densities while yielding lower bit error rates at lower SNR ratios at the expense of additional system complexity.

[0047] Exemplary case # 1 represents a link budget where the system transmitter voltage (e.g., transceiver 16 of Figure 4) is 2.4 volts peak-to-peak into a 50 ohm (+11.6 dBm), an 18 dB transmitting electromagnetic coupler 18 loss is used, the receiving electromagnetic coupler 18 has an additional 18 dB loss, and the Printed Circuit Board (PCB) and other system losses total 6 dB. In this case the desired link margin is 10 dB and the desired signal to noise ratio (SNR) is 25 dB. A conservative receiver implementation noise figure of 8 dB is assumed. Hence, the available noise bandwidth is over 10 GHz, corresponding to a 3 Giga-bit / second (Gb/sec) data rate at 0.3 bit per Hz of bandwidth. In this case, the signal level power would not necessarily be a limiting factor of the implementation.

[0048] Exemplary case # 2 represents a link budget where the transmitter voltage is reduced 6 dB to 1.2 volts peak-to-peak into a 50 ohms (+5.5 dBm), along with a more lossy 22 dB transmitting electromagnetic coupler 18, together with the receiving electromagnetic coupler 18 has, representing an additional 22 dB loss. The link margin of case # 2 has been decreased to a still conservative value of 8 dB. The noise figure of the receiver implementation has been increased to 9

the available noise bandwidth is 1.6 GHz, corresponding to a 480 Mb / second (Mb/sec) data rate assuming 0.3 bit per Hz of bandwidth.

[0049] Exemplary case #3 further reduces the transmitter voltage to 0.63 volts peak-to-peak (0 dBm) and further increases system implementation losses and reduces the link margin of the systems illustrated in Cases #1 and #2 above. Case #3 is representative of an even lower cost implementation that nonetheless supports a 81 Mb / sec data channel.

[0050] Together, exemplary cases #1 through #3 represent a broad range of operating conditions for various transmitter levels, receiver implementations and signal bandwidths. Many operating conditions outside the range of values of Table I could be implemented by those skilled in the art.

Table I

	Units	Case # 1	Case # 2	Case # 3
Voltage output of transmitter	volts p-p	2.4	1.2	0.63
RMS voltage = $V_{p-p} / 2.88$	volts rms	0.83	0.42	0.22
Transmitter output (milliwatts into 50 ohms)	dBm	11.6	5.5	0.0
Outgoing Coupling Loss	dB	18	22	26
Incoming Coupling Loss	dB	18	22	26
PCB and other System Losses	dB	6	6	6
RF Signal power at receiver	dBm	-30	-44	-58
Desired link margin	dB	10	8	6
Desired SNR	dB	25	20	15
Noise power budget	dBm	-65	-72	-79
Noise Figure Of receiver	dB	8	9	10
Noise Figure Of receiver (F)	ratio	6	8	10
Equivalent Noise Temperature $T_e = (F-1) \times 370$	degree K	1965	2569	3330
Available signal Bandwidth	Hz	10.6E+9	1.6E+9	270.8E+6
Bit Rate at 0.3 bit / Hz [BPSK]	Mb / Sec	3,168	481	81

[0051] The above described system is shown in FIG. 1, which is a cross-sectional view of the system showing the attenuation in dB between two 1.0 mm, 50 ohm microstrip lines over a shielding plane that is grounded. One trace, representing an electromagnetic coupler 18 of the above described system, is shown with a signal generator 19.

transceiver 16 of Figure 4) having an output impedance of 50 ohms and terminated with a 50 ohm resistor. The other trace, representing transmission line 22 in the above described embodiment, is terminated at both ends with 50 ohms of impedance. The spacing between the two microstrips is 0.05 mm (plot A) or 0.4 mm apart (plot B). Microstrip modeling was used for conservative and easy to model estimates of coupling; actual coupling values achievable with the broadside structures of this invention will yield less attenuation and/or smaller structures.

[0052] Again, it must be stressed that the above dimensions are exemplary only and given as a framework for the sample data present in Figure 7. The invention is not limited in any way to the above described dimensions or the sample data presented in Figure 7.

[0053] Figures 8-10 illustrate an alternative embodiment of an integrated circuit that may be used in electronics system 10. As shown in Figure 8, unlike the integrated circuit illustrated in Figure 4 which includes only one transceiver, integrated circuit 60 includes a plurality of transceivers 62(1)-62(x), each of which may be similar to the transceiver 16 illustrated in Figure 4. Like integrated circuit 14 in Figure 4, integrated circuit 60 may also include a logic circuit 12 and an input/output interface 64.

[0054] Generally speaking, integrated circuit 60 may be utilized in electronics system 10 in any manner that integrated circuit 14 of Figure 4 is utilized. Integrated circuit 60,

[0055] Figure 9 illustrates an exemplary configuration of an electronics system 52 in which a plurality of integrated circuits 14 are each having a transceiver 16. The

plurality of integrated circuits 60(1)-60(x) are mounted on a surface 65 of a printed circuit board 66. Embedded within the printed circuit board (and shown in dashed-outline form in Figure 5) are four transmission lines 76. As discussed above, the transmission lines 76 may alternatively be formed on the printed circuit board 66. Each of the four electromagnetic couplers 68 on each of the plurality of integrated circuits 60(1)-60(x) is coupled to one of the transmission lines 76. Preferably, each of electromagnetic couplers 62(1)-62(x) are disposed within approximately five millimeters of the its corresponding transmission line 76. The invention is not limited, however, to placement of any electromagnetic coupler 62 within five millimeters of a transmission line 76. In this manner, transmission lines 76 form a four-path, bus-like structure in which the plurality of integrated circuits 60(1)-60(x) can contactlessly communicate with each other over the bus-like structure.

[0056] As described above, the contactless communication paths in electronics system 10 of Figure 9 may optionally be fully or partially shielded. Figure 10 illustrates an exemplary embodiment with partial shielding of the electronics system 59 illustrated in Figure 9. As shown in Figure 10, a shielding plane 69 shields the circuitry on integrated circuit 60 from the four electromagnetic couplers 68 of integrated circuit 60. Each of the four transceivers 62 in integrated circuit 60 are electrically connected to an electromagnetic coupler 68 on the integrated circuit 60 through vias 67 extending through separate gaps in a shielding plane 69. Additional shielding may be provided by shielding planes or

73, between which transmission lines 76 are located as illustrated in Figure 6. If shielding plane 74 is included, gaps 75 in shielding plane 74 between each electromagnetic coupler 68 and each transmission line 76 are included in

shielding plane 74. Integrated circuit 60 can then be positioned on printed circuit board 66 so that its electromagnetic couplers 68 are electromagnetically coupled to the transmission lines 76 through the gaps 72.

[0057] As with the embodiment illustrated in Figures 1-4 above, one or more of the shielding planes may be grounded and may supply ground connections to the integrated circuits 60 or the printed circuit board 66. Similarly, one or more of the shielding planes may be connected to a power supply and supply power or a reference voltage to the integrated circuits 60 or the printed circuit board 66.

[0058] Although the embodiments of the invention described above contactlessly transmit data between integrated circuits, the present invention is not limited to the contactless transmission of signals between integrated circuits. Figure 11 illustrates an exemplary embodiment of an electronics system 79 in which signals are contactlessly transmitted between elements of an electronic system other than integrated circuits, namely, a daughter board and a mother board.

[0059] As shown in Figure 11, a plurality of daughter boards 86(1)-86(x) are physically mounted to a mother board 82. Conventional edge connectors 84 may be used to mount the daughter boards 86(1)-86(x) to the mother board 82. Each daughter card 86 includes a transmission line 90 embedded within or located on the daughter card 86. Motherboard 82 also includes a transmission line 89, which preferably is embedded in the mother board but may alternatively be located on the

which, when the daughter boards 86 are mounted to the mother board 82, are positioned in proximity to the transmission line 89 in the mother board 82 such that the electromagnetic coupling between the daughter boards 86 and the mother board 82 is maximized.

coupled to the transmission line 89 of the mother board 82. Preferably, an electromagnetic coupler 92 is disposed within approximately five millimeters of its corresponding transmission line 90. The invention is not limited, however, to placement of any electromagnetic coupler 92 within five millimeters of a transmission line 89. In this manner, daughter boards 86 can contactlessly communicate with mother board 86.

[0060] The contactless communications paths illustrated in Figure 11 may optionally be partially or fully shielded. Figure 12 shows a partial, sectional view of a daughter board 86 and mother board 82 and illustrates exemplary shielding that may be utilized to partially shield the embodiment illustrated in Figure 11. As shown in Figure 12, a shielding plane or shielding via 202 may be disposed within daughter board 86 to shield the daughter card from electromagnetic coupler 92. The amount of shielding, of course, depends, among other things, on the degree to which the electromagnetic coupler 92 is surrounded by shielding material and thereby electromagnetically isolated from other elements on the daughter board 86. As with other embodiments described herein, persons skilled in the field will thus be able to adjust the degree by which daughter card 86 is shielded from electromagnetic coupler 92 by selective placement of shielding planes or materials around electromagnetic coupler 92. In the example illustrated in Figure 12, shielding plane 202 includes a gap 210 through which transmission line 90 is electrically connected to electromagnetic coupler 92.

0061

disposed between shielding planes 204 and 205. As also shown, shielding plane 204 includes a gap 206 in proximity to electromagnetic coupler 92, allowing for a contactless communication path through the gap between the daughter

coupler 92 and transmission line 89. Transmission line 89 may be more fully shielded by including additional shielding planes that more fully enclose the transmission line. For example, additional shielding planes may be included in front of the transmission line 89 and behind the transmission line (from the perspective of Figure 12). As discussed above, the shielding planes may be connected to power or ground to provide power or reference signals to the mother board 82 or daughter board 86.

[0062] Daughter boards 86 may include integrated circuits or other system elements that are electromagnetically coupled to transmission line 90 in accordance with the principles of the present invention. For example, daughter boards 86 may include configurations as described above with respect to Figures 1-10. Alternatively, daughter boards 86 may include integrated circuits or other system elements that are conventionally coupled via contact connections to transmission line 90. Of course, daughter boards 86 may include multiple traces 90, and the connections between each such trace and system elements on the daughter cards may include a combination of contactless connections and conventional contact connections.

[0063] It should be noted that the present invention does not require that transmission lines be arranged into any particular bus structure. Figures 13 and 14 illustrate an exemplary embodiment of the invention that utilizes a daisy-chain or ring type bus arrangement.

[0064] As shown in Figure 13, an integrated circuit 90 may be configured with a transmitting coupler 102 and a separate

circuit 90 may also include a logic circuit 92 communicating via an input/output interface 94. A receiver 96 demodulates an RF signal arriving at an electromagnetic coupler 98 to produce an RF signal that is input to the interface 94. Alternatively, the

input signal 95 conveys data transmitted by another element that is electromagnetically coupled to the ring bus. If the data is addressed to integrated circuit 90, input/output interface 94 passes the data to logic circuit 93. Otherwise input/output interface 94 encodes the data into an output signal 97 and passes it to transmitter 100. Transmitter 100 supplies an RF signal modulated by the output signal 97 to an electromagnetic coupler 102.

[0065] Input/output interface 94 also encodes any data originating from logic circuit 93 to be sent to another element on the ring bus. Input/output interface 94 encodes the data along with the address of the intended recipient of the data and delivers an encoded output signal 97 to transmitter 100, which transmits the encoded signal onto the ring bus.

[0066] Figure 14 is a simplified cross-sectional view of a printed circuit board 104 holding several integrated circuits 90 similar to integrated circuit 90 of Figure 13. Separate short traces 106 embedded in or located on the printed circuit board 104 electromagnetically couple pairs of couplers 98 and 102 on adjacent integrated circuits 90. Shielding such as that discussed above with respect to other embodiments of the invention may also be included. For example, a shielding plane 108 may shield traces 106 from one another. Although not shown in Figure 14, printed circuit board 90 may also include shielding planes above and below traces 108, and integrated circuit 90 may include a shielding plane above electromagnetic couplers 98 and 102 and below the circuits implemented on the substrate of integrated circuit 90 to provide shielding.

[0067] Although the present invention is described in terms of the invention utilize a transmission line as an intermediary bus-like structure in communications between integrated circuits, the present invention is not limited to contactless

transmissions involving a transmission line or any type of bus arrangement.

[0068] Figures 15-16 illustrate an exemplary embodiment of the invention in which integrated circuits contactlessly communicate directly with each other. As shown in Figure 15 (a cross-sectional side view), a plurality (in this example three) of integrated circuits 112(1)-112(3) are vertically stacked. For example integrated circuit 112(3) might include a computer processor and integrated circuits 112(1) and 112(3) might implement memories the processor accesses. Each integrated circuit 112(1)-112(3) includes a substrate 116 in which is formed circuitry. For example, the circuitry might include a logic circuit, an input/output interface, and a transceiver or transceivers configured in an arrangement similar to that of integrated circuit 14 of Figure 4, integrated circuit 60 of Figure 8, or integrated circuit 90 of Figure 13. The transceiver in each integrated circuit 112(1)-112(3) is connected to a corresponding electromagnetic coupler 118(1)/118(3), which is preferably formed on or within substrate 116. Electromagnetic couplers 118(1)-118(3) are located in proximity with each other so as to be electromagnetically coupled with one another. In this manner, integrated circuits 112(1)-112(3) communicate with each other contactlessly through the silicon without requiring vias or conductive vertical elements to interconnect the stacked dice.

[0069] Integrated circuits 112(1)-112(3) can be disposed such that each electromagnetic coupler 118(1)-118(3) is electromagnetically coupled to all of the other electromagnetic

pass RF signals vertically in either direction between electromagnetic coupler 118(1) and 118(3) without minimum attenuation. In such arrangements, a transmission by one integrated circuit 112(1) will be received and processed by all of

the other integrated circuits. Only the integrated circuit to which the transmission was addressed, however, would keep and process the data in the transmission.

[0070] Alternatively, each integrated circuit 112 could be disposed (and or shielded) such that its electromagnetic coupler 118 is electromagnetically coupled only to the electromagnetic coupler of the integrated circuit immediately above and/or below. A communications protocol such as that described above with respect to Figures 13 and 14 could be used. For example, upon receiving a transmission from a neighbor, an integrated circuit 118 decodes the destination address of the transmission. If the transmission is addressed to the integrated circuit, the integrated circuit decodes and processes the data in the transmission. If, however, the transmission is not addressed to the integrated circuit, the integrated circuit forwards the transmission to its other neighbor.

[0071] Optional shielding may be included. For example, shielding planes 126 may be included in integrated circuits 118(1)-118(3) to shield the circuitry in each integrated circuit from electromagnetic couplers 118(1)-118(3). If such shielding planes 126 are included, gaps 128 in the planes should be included between electromagnetic couplers 118(1)-118(3). Additional shielding may be included in accordance with the shielding principles discussed above to provide more complete shielding.

[0072] As illustrated in Figure 15, the stacked integrated circuits 112(1)-112(3) may make conventional physical contact type electrical connections with printed circuit board 114. Alternatively, the stacked integrated circuits 112(1)-112(3) may communicate inductively with the printed circuit board 114. In the latter

arrangement is illustrated in Figure 15. There the integrated circuit 112(3) communicates contactlessly with the printed circuit board 114, and the printed circuit board includes optional shielding in accordance with the shielding principles discussed above. As also illustrated in Figure 15, power and ground connectors 115 may be included to provide power, ground, and reference voltage connections to the integrated circuits 118(1)-118(3).

[0073] As shown, printed circuit board 114, which may be similar to printed circuit board 21 of Figure 3, includes a trace 120 disposed between two shielding planes 122 and 124. The trace 120 may convey a radio frequency signal to other electronic elements on the printed circuit board 114. Examples of other circuit elements include without limitation other integrated circuits or other stacks of integrated circuits. The electromagnetic coupler 118(3) of integrated circuit 112(3) resides in proximity to a gap 121 in optional shielding plane 122 so that coupler 118(3) is electromagnetically coupled to trace 120. In accordance with the shielding principles discussed above, additional shielding planes or materials may be included to more fully shield trace 120 and the contactless communication path between electromagnetic coupler 118(3) and trace 120.

[0074] Figure 16 illustrates an exemplary embodiment in which one portion of an integrated circuit 130 communicates contactlessly with another portion of the integrated circuit. As illustrated, an integrated circuit 130 has circuits formed on both top and bottom surfaces of its semiconductor substrate

arrangements similar to that of integrated circuit 14 of Figure 4, integrated circuit 60 of Figure 8, or integrated circuit 90 of Figure 13. An electromagnetic coupler 134 is associated with circuitry on one side of the substrate 132, and a second

electromagnetic coupler 140 is associated with circuitry on the other side of the substrate. In this manner, couplers 134 and 140 are electromagnetically coupled so that the circuitry on one side of the substrate 132 can contactlessly communicate with circuitry on the other side of the substrate.

[0075] Full or partial shielding may optionally be included in the embodiment illustrated in Figure 15. For example, shielding planes 138 and 144 may be appropriately disposed to shield the integrated circuits on both sides of the substrate 132 from the couplers 134, 140. Additional shielding may be included in accordance with the principles discussed above to more fully shield the integrated circuits from the couplers 134, 140. Gaps are provided in the shielding as needed to allow couplers 134 and 140 to couple electromagnetically to each other.

[0076] As with the "stacked" integrated circuit embodiment illustrated in Figure 15 above, the dual-sided embodiment illustrated in Figure 16 may be mounted on a printed circuit board 146. As discussed above with regard to Figure 15, conventional physical contact structures may be used to communicate with the printed circuit board 146, or contactlessly coupling in accordance with the instant invention may be used. Figure 16 illustrates the latter with optional shielding planes 302, 304 for shielding transmission line 148. Gap 306 is provided in shielding plane 302 to allow coupler 140 to couple to transmission line 148. As discussed above, additional shielding could be added to more fully shield trace 148.

[0077] connectors (such as 115 in Figure 15) may be included to provide power, ground, and reference voltage connections to the integrated circuit 130. In addition, multiple integrated circuit similar to integrated circuit 130 with circuitry

integrated into both sides of a substrate) may be stacked as shown in Figure 15.

[0078] Of course, two or more integrated circuits each having an electromagnetic coupler may simply be disposed such that their electromagnetic couplers are in sufficient proximity that they are electromagnetically coupled to each other. Preferably, electromagnetic couplers that are electromagnetically coupled to each other are disposed within approximately twenty-five millimeters of each other. The invention is not limited, however, to placement of any electromagnetic coupler within twenty-five millimeters of any other electromagnetic coupler.

[0079] Figures 17a to 17c illustrate exemplary arrangements in which two or more integrated circuits are arranged with a direct wireless communication path or channel between two or more integrated circuits. The integrated circuits may be mounted on a printed circuit board (not shown) or other substrate or frame suitable for securing integrated circuits. In these exemplary arrangements, an electromagnetic coupler is formed on an outer edge of the integrated circuit. In Figure 17a, integrated circuits 600 and 604 are arranged such that they can wirelessly communicate with each other. In Figure 17b, three integrated circuits 610, 614, 418 are arranged such that each is able to wirelessly communicate with the other. In Figure 17c, one integrated circuit 630 includes four electromagnetic couplers, each arranged to be electromagnetically coupled to one of integrated circuits 634-646.

[0080]
above, shielding materials may optionally be included and disposed so as to fully or partially shield or "close" one or more contactless communications channels between the integrated circuits. For example, shielding material is shown in

Figures 17a-17c) may be placed between the electromagnetic couplers 601, 602, 611, 612, 613, 660, 662, 664, 666, 668, 670, 672, 674 and circuitry on any of the integrated circuits shown in Figures 17a-17c as generally described above. As illustrated in Figure 17a, shielding planes or traces 606, 608 may also be disposed so as to shield the contactless communication path between two or more coupled electromagnetic couplers 601, 602. Although not shown in Figure 17a, additional shielding planes or traces may be included above and below (from the perspective of Figure 17a) integrated circuits 600 and 604 to more fully shield the contactless communication path between couplers 601 and 606. Similarly, shielding material 620, 622 may be disposed to shield the contactless communication paths among couplers 611, 612, 613 in Figure 17b, and additional shielding (not shown) may be included above and below (from the perspective of Figure 17b) the integrated circuits 610, 614, 618 to more fully shield the paths. Figure 17c likewise shows exemplary shielding material 650 shielding the contactless communication path between couplers 660 and 662 from the contactless communication path between couplers 664 and 666. Shielding material 648, 654, 652 similarly shields contactless communication paths between the following pairs of couplers: 668 and 670, 672 and 674, and 650, 652. Additional shielding material (not shown) could be placed above and below (from the perspective of Figure 17c) the coupling areas between adjacent couplers to more fully shield the contact communication paths.

[0081] Although the electromagnetic couplers illustrated in Figures 1-17c are shown as being formed by straight line

shape and size of the electromagnetic couplers can be selected to cause predetermined levels of inductance and capacitance so as to form tuned or resonate circuit or radio-frequency transmission structures. Moreover, a boundary or a width

respect to integrated circuits illustrated in Figures 1-16, electromagnetic couplers 601, 602, 611, 612, 613, 660, 662, 664, 666, 668, 670, 672, 674 are preferably formed on or within the integrated circuit using standard semiconductor fabrication techniques. Alternatively, the electromagnetic couplers can be fabricated as part of the semiconductor package.

[0082] Figure 18 illustrates an exemplary spiral coupler 404 formed on or within a semiconductor substrate 402. Also formed on the substrate is transceiver circuitry 406 and functional circuitry 408, which may be similar to like circuits illustrated in Figures 4, 8, and 13. Figure 19 illustrates a circuit modeling equivalent impedances of the spiral coupler 404 and the transceiver circuitry 406 and a transmission line (not shown in Figure 18) or like coupler to which the spiral coupler 404 is coupled. In Figure 19, L_1 and C_1 represent the inductance and capacitance of spiral conductor 404 and its connection path to transceiver circuitry 406; R_1 represents the input or output impedance of the transceiver circuitry 406; and L_2 and C_2 represent the inductance and capacitance of the transmission line (not shown in Figure 18) to which the spiral conductor 404 is coupled.

[0083] The spacing between spiral coupler 404 and the transmission line or like coupler may be selected to provide a link coupling factor k in the range of slightly more than 0.0 to 1.0. Inductances L_1 and L_2 and capacitances C_1 and C_2 may be sized to resonate at the frequency f_c of the RF carrier signal as follows:

[0084] The input impedance R_1 of the transceiver is selected to obtain a desired circuit quality factor Q as follows:

[0085] Higher values of Q increase the amount of coupling between the spiral coupler 118 and the transmission line but also decrease RF signal bandwidth. The appropriate choice for Q thus depends on the amount of signal attenuation the transceivers can tolerate.

[0086] If tight coupling between electromagnetic couplers is desired, the electromagnetic couplers may be one fourth of the wavelength of the RF signal carrier frequency, thus forming a resonate coupling structure. As noted above, however, if electromagnetic couplers of a large number of integrated circuits are tightly coupled to a transmission line, each electromagnetic coupler draws a substantial amount of power from the radio frequency signal as it travels along the transmission line and the radio frequency signal can become severely attenuated by the time it reaches an integrated circuit at the end of a transmission line. As discussed above, in such a case, it is preferable to size electromagnetic couplers to be less tightly coupled to the transmission line so that they do not draw substantially more power than needed to permit an incoming radio frequency signal to be properly detected by the transceivers. Where only a few integrated circuits are contactlessly connected to each other, it may be advantageous to increase the coupling between integrated circuits to reduce the amount of radio-frequency energy radiated and hence reduce shielding requirements.

[0087] While the forgoing specification has described preferred embodiment(s) of the present invention, one skilled

in the art will recognize that the invention may be embodied in other aspects. For example, it should be understood that invention can be used in combination with conventional data communication techniques. For example, the electronics systems described herein may include integrated circuits and other system

elements that communicate conventionally via physical contact. As another example, any of the transceivers described above may be replaced with a circuit that only transmits or only receives as may be appropriate in a given application of the invention. In addition, the input/output interface 15, 64, and 94 in Figures 4, 8, and 13, may be configured to communicate with other elements in addition to a transceiver and a logic circuit. Also, in Figure 3, integrated circuits 14 could be mounted on both sides of the printed circuit board 21, and gaps provided in shielding plane 48 to allow couplers 18 on integrated circuits mounted on the lower (from the perspective of Figure 3) of printed circuit board 21 to electromagnetically couple with transmission line 22. Similarly, integrated circuits or stacks of integrated circuits could be mounted on both sides of the printed circuit boards shown in Figures 10, 14, 15, and 16.